



GSTIN : 20AAAAI0686D1ZA

भारतीय प्रौद्योगिकी संस्थान (भारतीय खनि विद्यापीठ), धनबाद**धनबाद, झारखण्ड, भारत, पिन-826004**

(शिक्षा मंत्रालय, भारत सरकार के अधीन राष्ट्रीय महत्व का एक संस्थान)

INDIAN INSTITUTE OF TECHNOLOGY (INDIAN SCHOOL OF MINES), DHANBAD**DHANBAD, JHARKHAND, INDIA, PIN-826004****(An Institute of National Importance under Ministry of Education. Govt. of India)****PROJECT PURCHASE SECTION****Phone: (0326)2235660 || Email: projectpurchase@iitism.ac.in || Website: www.iitism.ac.in****No. EC-PRJ-045-25-26****Date: 3rd December 2025****Request for Quotation Corrigendum-1**

Reference No.: EC-PRJ-045-25-26 dated 02.12.2025

Subject: Supply & Installation of Path ADS Core, EM Design Core, Layout, RF Pro.

<i>Sl. No</i>	<i>As per Existing RFQ</i>	<i>Now Amended as</i>
1.	Supply & Installation of Path ADS Core, EM Design Core, Layout, RF Pro. License Type: Node Lock. Validity: Minimum 2 years Supported Operating System: Window 10,11 Enterprise (64-bit) Warranty & Support: Updates and technical support should be provided free of cost for 1 year. Quantity: 01 No. (52 User Network License)	Supply & Installation of Path ADS Core, EM Design Core, Layout, RF Pro. License Type: Node Lock. Validity: Minimum 2 years Supported Operating System: Window 10,11 Enterprise (64-bit) Warranty & Support: Updates and technical support should be provided free of cost for 1 year. Quantity: 01No.

*The others terms & condition of the tender will remain the same***For and on behalf of
IIT(ISM), Dhanbad**Deputy Registrar
(Project Purchase Section)
O/o Dean (R&D)

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Indian Institute of technology (Indian School of Mines), Dhanbad is interested for the purchase of the materials/ equipment listed below:

Sl. No.	Detailed Specifications	Quantity
1	Supply & Installation of Path ADS Core, EM Design Core, Layout, RF Pro. License Type: Node Lock. Validity: Minimum 2 years Supported Operating System: Window 10,11 Enterprise (64-bit) Warranty & Support: Updates and technical support should be provided free of cost for 1 year. (Detailed Specification is given in Annexure –I)	01 No. (52 User Network License)

INSTRUCTIONS:

- 1) Please attach relevant technical literature of the item.
- 2) Please attach authorization certificate for OEM.
- 3) Please mention warranty/ guarantee period in your offer. Equipment/ material supplied must have minimum warranty/ guarantee of **12 months**
- 4) Price Basis: FOR IIT (ISM) Dhanbad
- 5) The items/ materials shall be required to be delivered at **Department of Electronics Engineering of IIT (ISM) Dhanbad** at the risk and cost of the tenderer.
- 6) ***Please attach a certificate that the quoted price is not more than that of any other Govt. organization/ institution in India. This has to be mentioned in the offer letter clearly.***
- 7) The rates should be quoted for each item separately as per price schedule attached as annexure I
- 8) The items/ materials shall be required to be delivered at Electronics Engineering Department of IIT (ISM) Dhanbad at the risk and cost of the tenderer.
- 9) Your tender must be **valid for minimum 180 days** from the date of opening of tender.
- 10) Full details of items/materials offered should be given in the tender along with supporting & relevant literatures/ Technical Literature.
- 11) Tender may please be submitted ***in sealed cover only superscribed with Enquiry No. EC-PRJ-045-25-26 latest by 22.12.2025.***
- 12) **EMD:** EMD of an amount of **Rs. 17000/-** in form of Demand Draft in favor of Registrar, IIT (ISM) Dhanbad payable at Dhanbad. EMD can also be submitted in the form of Term Deposit Receipt/ Fixed Deposit Receipt/Bank Guarantee drawn in favor of Registrar, IIT(ISM)Dhanbad
- 13) **Performance Security:** To ensure due performance of the item, you are required to submit a performance security. The performance security should be furnished within 21 days of the award of supply order. Performance security should be for an amount of five percent (5%) of the value of the supply order. Performance security may be furnished in the form of a Demand Draft/ Fixed Deposit Receipt / Bank Guarantee from a commercial bank in favour of Registrar IIT (ISM) Dhanbad, payable at Dhanbad. Performance security should remain valid for a period of 14 Months from the date of final acceptance/Installation. The performance security will be refunded without any interest, provided that the performance is satisfactory.
- 14) The offer must be submitted in the office of Deputy Registrar (Purchase & Stores), IIT (ISM), Dhanbad– 826004 (Jharkhand, India) only. Please send your offer by Registered Post/ Speed Post/ Courier along with Courier receipt. Tender/ quotation will be received during IIT (ISM) Dhanbad working hours only (i.e.

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Monday to Friday). At any circumstances by hand delivery is not acceptable. Late or delayed tenders shall be summarily rejected. Bids sent through Email/Fax or submitted in unsealed cover(s) will not be accepted and such bids will be treated as non-responsive bids.

- 15) Any other information that you may like to obtain, you are free to contact IIT (ISM) Dhanbad before submission of tender.
- 16) IIT (ISM) Dhanbad reserves the right to accept and/or to reject any/all tenders without assigning any reason.
- 17) **Payment:** Payment shall normally be made within 3-4 weeks subject to satisfactory supply, submission of tax invoice and delivery challan in triplicate (as per Purchase Order Terms) of the ordered materials/items. ***Advance payment is not admissible.***
- 18) You are requested to fill the enclosed **Annexures**.
- 19) **Please attach purchase order copies of the same equipment which you have supplied to any other Govt., public sector and autonomous institutions.**
- 20) **a) In a tender, either the Indian agent on behalf of the principle/ OEM and the Principle/OEM itself can bid but both cannot bid simultaneously for the same item/product in the same tender.**
b) If an agent submit bid on behalf of the principle/ OEM, the same agent shall not submit a bid on behalf of another principle/ OEM in the same tender for the same item/product.

**For and on behalf of
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Deputy Registrar
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Sr No	Specifications
1.	Supported Operating System: Software should support installation and usage of Software natively on following operating systems: - Windows 10, 11 Enterprise (64-bit) - Linux RHEL 8
2.	<u>ADS EM and RF Design Environment</u> - Design Environment should be graphical user interface for capturing the Design Environment including the configuration of simulation and optimization, setting variables, viewing libraries etc. - EM Design enables the creation and import of parameterized 3D components into ADS
3.	<u>Data Display</u> Data Display should allow simulation results to be viewed either graphically and Support a full range of post processing automation. - The data display should provide multiple measurement graphs on the same display Page. - The data display should support content customization and custom Formatting. - Single Data Display Window should support multiple pages to plot separate data graphs in respective pages
4	Software should provide inbuilt RFIP encoder to encrypt design file for sharing with others to protect the confidential design information. Software to support Python scripting.
5	<u>Linear Simulator:</u> - Software should Linear Simulation capability such as S-Parameter and AC Simulator - Software simulator should be able to perform Swept Parameter Analysis, such as noise figure versus a change in a component value. - Software should provide in-built interactive Smith Chart utility for Broadband or Narrow band impedance matching networks design. - Software Simulator should enable designer to create EM-based multi-dimensional, parameterized models for passive planar components. Model types include standard interconnect components such as opens, stubs, bends, and tees on custom substrates. - Winslow probe to be supported.
6	<u>Non-Linear Simulator</u> Non-Linear Simulator should support frequency-domain, steady-state, large signal analysis of non-linear circuits excited with multi-tone sources. - It should have capability to simulate noise, gain compression, harmonic distortion, oscillator spurs, phase noise, and inter-modulation products in non-linear circuits. - Simulation software should provide facility to perform RF System level simulation like Transmitters and Receivers etc. Software should have flexibility to include actual circuit level design as a sub-circuit that can be tuned or optimized to achieve System level specifications. - Should have X-Parameter Simulation capability. - Error Vector Magnitude (EVM) optimization at circuit level. - Comprehensive nonlinear stability analysis. - Should provide efficient simulation technique for circuits excited with complex digitally modulated RF signals. - Should include templates for designing linearizers, RF systems, and PLL Systems.



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	Should be useful for designing amplifiers that must perform efficiently for wideband digitally modulated RF signals typical of 5G, 6G and radar applications.
7	3D Planar Electromagnetic Simulator (Method of Moments) 3D planar EM simulation is needed for passive circuit analysis. It should accept arbitrary design geometries (including multi-layer structures) and accurately simulates complex EM effects including coupling and parasitic. Accurate EM simulation should enable MMIC designers to improve passive circuit performance and increases confidence that the manufactured product will function as simulated. - Software should provide RF and MW mode of EM simulation - It should support automatic edge mesh for improved behavioral characterization of current crowding in high frequency signals. - Method of Moments EM Simulator should support simulation of Bond Wires - EM Simulator should allow minimum of 16-cores multithreaded simulation - EM Simulator must support following options for accurate High Frequency EM simulations: - Hammerstad & Hemispherical roughness models for conductors - Frequency Dependent Dielectric Definition - Through Silicon VIAs - Should support Differential Meshing where user can select: Net Specific Meshing, where designer should have the capability to define meshing criteria of each Net in the design. Shape Specific Meshing, which shall allow user to select any shape in layout to be meshed differently than the global mesh setting. Layer Specific: User should be able to define different meshing criteria for different layers EM simulator should support Remote EM simulation on Linux Operating System where user on Windows machine should be able to send large EM simulation jobs on remote Linux machine for simulation as an extended capability
8	Electromagnetic Optimization Software should support full range of 3D Planar Electromagnetic Optimization with min. of following optimization techniques: - Random - Gradient - Hybrid - Genetic - Discrete Software should have the capability to parameterize Substrate parameters like Er, Height, TanD etc for parametric EM simulations as well as EM Optimization
9	Software should have Statistical Design capabilities to provide designers with following capabilities: - Sensitivity Analysis - Yield Analysis - Yield Optimization - Design of Experiments (DOE) - Yield Sensitivity Histograms



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10	<u>Layout features:</u> Layout should be comprehensive physical design environment specifically geared for RF circuit development. It should support advanced layout editing features with many of the RF/Microwave technologies integrated in the tool to be used in the development of RFIC, MMIC, PCB layouts. • Tool should support import and export DXF and DWG file format in the layout • Tool should provide DRC capability to run design rule checks to catch any potential layout routing mistakes as per the foundry rules. • Tool should support direct import of Cadence Allegro. brd into layout for EM simulation • Tool should support import & export of Gerber files • Tool should support import & export of ODB++ files • Layout to layout comparison be to supported Layout should provide command line editor feature to perform normal layout editing operation directly from command line instead of using layout icon which are time consuming Layout should provide following Views (mandatory): Component Palette: Common Items like Microstrip lines, Lumped components, Transistors etc in Layout Window <u>Navigator Window:</u> To see all the Nets in the layout <u>Info Window:</u> To see all relevant information of select part in layout <u>Search Window:</u> To search for any component throughout the hierarchy of layout objects.
11.	Software should provide advanced model creator which shall allow generating custom, arbitrary shaped parameterized multilayer passive models library and not restricted to standard shapes and discontinuities created using 3D Planar EM or Full 3D EM solvers which can be used for more accurate circuit / system level designs.
12	<u>Advance Model composer:</u> Tool should provide advanced model creator which shall allow generating custom, arbitrary shaped parameterized multilayer passive models library and not restricted to standard shapes and discontinuities created using 3D Planar EM or Full 3D EM solvers which can be used for more accurate circuit / system level designs.
13	Software must provide an efficient import of complex 3D CAD designs and the ability to perform the setup of complex CAD designs for complete 3D FEM simulation. Some of the must have import file formats are: ▪ ProE ▪ ProE Assembly ▪ VDA FS ▪ Inventor ▪ CATIA v4 and v5
14	Software must include the following capabilities to ensure ease-of-use, fastest simulation time and highest degree of simulation accuracy: ▪ Interactive 3D drawing and editing capabilities to enable the completion of the 3D drawing quickly. ▪ Easy direct parameterization of any dimension that allows replacement of any dimension with an equation variable. ▪ Viewing of dynamic real time plots of intermediate results to provide early insights to the performance. Simulation setup must have built-in intelligence to launch simulations correctly with minimal user input but still gives full control if needed.

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15	Software should support Python scripting language to create and parameterize complex 3D models using the script. Also, the script should support Ports, Material definition, Simulation setup and post processing modes to allow designers to have the complete control of simulation using the script interface without having the need for user to do all these operations manually.
16	Should provide automatic expert settings for accurate EM simulation of design for correct results - Software should set mesh, port, ground and boundary parameters automatically for quick accurate simulation. - Software should not require cookie-cutting and cleanup of EM structures for external EM simulators
17	<u>Full 3D Finite Element Method (FEM) Simulator</u> - Software should provide fully integrated Full 3D EM simulator based on FEM technique in the same GUI and its capabilities should include: - Full wave 3D FEM EM simulator with advanced direct and iterative solver for speed and capacity - Adaptive frequency sweep to locate all resonant frequencies automatically and quickly with minimum simulation frequency points - Automatic edge-based meshing for transmission line objects Symmetry planes to speed up simulation and increase capacity - FEM simulator should be multi-threaded with support for at least 16-cores of multithreading - Software should support broadband mesh refinement to ensure accurate result if designs have resonances at lower frequencies - Software should allow specifying more precise initial mesh settings. Accuracy and convergence speed should be improved by creating a more refined initial mesh near vertices, and near conductor edges and surfaces. - FEM solver should allow for Djordjevic dielectric loss modeling which is one of the broadly accepted dielectric loss models in the industry - Software should allow individual Mesh settings for Global Vertex, Edge and Surface Meshing of 3D object 3D parameterized components of commonly used structures such as wire bonds, solder balls, solder bumps, connectors and packages to speed up 3D design input and enables geometry sweeps and co-optimization with circuit components
18	Software should provide the capability to perform multi-technology simulation in one circuit and EM design environment. It should be possible to integrate RFIC, MMIC, PCB, Bondwire technologies into 1 EM simulation natively inside the software platform without having the need to export/import any objects/parts from the main design environment. Vendor should supply or attached published presentation slides, application note or any published reference showcasing a typical Multi-Technology Circuit and EM simulation setup natively inside Circuit Simulation Software.
19	<u>Single EM Analyzer GUI/cockpit window : RFPro</u> Software should provide EM Analyzer cockpit with following features within 1 EM Analyzer GUI/cockpit window: - Support for Method of Moments solver (both Quasi Static & Full Wave) - Support for Full 3D FEM solver - Finite Element Method (FEM) - It should be possible to select Circuit or EM instance for each component on the fly



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	- Should support Python scripting to set up simulation and other options to automate the flow - Should back-annotate the EM results to create Schematic automatically from EM Analyzer window - Analyzer should support Dynamic Current Visualization to inspect Current Distribution with Schematic based Active & Passive components in the EM domain - Analyzer should support EM Far-Field Visualization in presence of Schematic based Active and Passive components - Analyzer should support for full EM extraction as well as Nets based selection to simulate only a specific portion of the layout to be EM simulated - It should be possible to insert Virtual Simulation Pins in the overall layout to observe simulation results for complete layout as well as for that specific layout portion/net separately - Analyzer should support distributed EM simulation on compute farms such as LSF etc - It should be integrated into Cadence Virtuoso, enabling RFIC and MMIC designers using Virtuoso to appreciate the same ease-of-use and spontaneous access to EM simulation as in ADS for EM-circuit Cosimulation. - User interface should run EM analysis as easily as circuit simulation - It should always have correct settings of EM analysis for RF/Microwave circuit design - It should have interactive EM-Circuit co-simulation Analysis, Tuning and Optimization - It should have Integrated environment and should not need external EM simulators - It should have effortless EM analysis setup with no layout modification i.e. "cookie cutting" & cleanup for - It should support error free assignment of EM ports and materials; user should not involve with tedious re-import and stitching of EM results for EM-Circuit co-simulation - It Should support Process variations simulation without parameterizing (Over-etching and under-etching and Mask registration) - User should be able to insert 3D components to perform simulations and should be able to insert Encrypted or plain 3D components to reuse them - User should be able to add Solder balls and bond wires natively inside tool It should allow State of the art job submission that works with all load sharing tools and cloud architectures as well as jobs can be submitted from Windows or Linux with no live connection between the submitting and the simulation machines - It Should have shorter Simulation runtime with quicker and more robust computation of the substrate database - It should have Mesh Domain Optimization (MDO) with supplements net extraction by automatically reducing the simulation space of large grounds and power planes to what's needed - It should support waveguide and the delta-gap ports
20	<u>S-Parameter Checker / Viewer</u> Software should provide following features: - S-Parameter File Viewer without having the need to simulate S-Parameter files - It should provide capability to check Reciprocity and Passivity of S-Parameter files - Should provide display of Magnitude as well as Phase response of S-Parameter

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21	<u>Localized support</u> ▪ The EDA vendor shall offer strong regional support ▪ Technical support shall reside both remotely and locally ▪ Local customer support services shall be available ▪ A web-based knowledge center should be provided ▪ Complete Solutions should be from same OEM. ▪ Should have seamless integration of simulation software and Keysight hardware as there is tight integration between simulation & measurement.
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To,
The Registrar,
Indian Institute of Technology (Indian School of Mines)
Dhanbad -826004
Respected Sir,

In accordance with the order No. P-45021/2/2017-PP (BE-II) dated 04th June, 2020, I hereby declare that

i) I am aware about all provision mentioned in GeM Bid No.as well as order No. P-45021/2/2017-PP (BE-II) dated 04th June, 2020 and abides by the same.

ii) I declare that for this tender, I am a **Class-I local supplier / Class-II local supplier / Non-local supplier** (Strike out whichever is not applicable) and classification is based on local content of goods/services/work offered by bidder in this tender.

iii) **Local content (in percentage) in offered good/services/work is: _____%**

Whereas 'Local Content' means the amount of value added in India which shall, unless otherwise prescribed by the Nodal Ministry, be the total value of the item procured (excluding net domestic indirect taxes) minus the value of imported content in the item (including all customs duties) as a proportion of the total value, in percent.

iv) The local content for all inputs which constitute the said goods/services/works has been verified and bidder is responsible for the correctness of the claims made therein.

v) Details of items, amount and location(s) at which the local value addition is made:

Sl. No.	ITEM (S)	AMOUNT	LOCATION(S)
1.			
2.			
3.			
4.			
5.			
6.			
7.			

Date:

Signature:

Name of Authorized Signatory:

Name of Bidder:

Seal of Bidder:

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1. I / We _____ (Tenderer) hereby declare that the firm / agency / Company, namely M/s _____ has not been declared as **insolvent**.

AND

2. I / We _____ (Tenderer) hereby declare that the firm / agency / Company, namely M/s _____ has not been **blacklisted or debarred** in the past by Union / State Government or Organization from taking part in Government tenders in India.

OR

2. I / We _____ (Tenderer) hereby declare that the Firm/Agency/Company, namely M/s _____ was **blacklisted or debarred** by Union/ State Government or any Organization from taking part in Government tenders for a period of _____ years w.e.f. _____ to _____. The period is expired and now the Firm/Agency/Company is entitled to take part in Government tenders.

In case the above information is found to be false I / We are fully aware that the tender/contract will be rejected / cancelled by Director, IIT(ISM) Dhanbad, and EMD shall be forfeited.

In addition to the above the Director, IIT(ISM) Dhanbad will not be responsible to pay the bills for any completed/partially completed work.

Date:**Place:****Self Certification:** _____**Name:** _____**Address:** _____**Stamp:**

**भारतीय प्रौद्योगिकी संस्थान (भारतीय खनि विद्यापीठ), धनबाद**

धनबाद, झारखण्ड, भारत, पिन-826004

(शिक्षा मंत्रालय, भारत सरकार के अधीन राष्ट्रीय महत्व का एक संस्थान)

INDIAN INSTITUTE OF TECHNOLOGY (INDIAN SCHOOL OF MINES), DHANBAD**DHANBAD, JHARKHAND, INDIA, PIN-826004****(An Institute of National Importance under Ministry of Education. Govt. of India)****PROJECT PURCHASE SECTION** Phone: (0326)2235660 || Email: projectpurchase@iitism.ac.in || Website: www.iitism.ac.in**Annexure- IV****UNDERTAKING****(To be signed and submitted/uploaded along with Technical bid documents on Company's Letter Head)****(As per Office Memorandum No. F.7/10/2021-PPD(1) dated: 23.02.2023 issued by Department of Expenditure (Ministry of Finance), Govt. of India)****Model certificate for Tenders**

"I have read the clause regarding restrictions on procurement from a bidder of a country which shares a land border with India; I certify that this bidder is not from such a country or, if from such a country, has been registered with the Competent Authority. I hereby certify that this bidder fulfills all requirements in this regard and is eligible to be considered. [Where applicable, evidence of valid registration by the Competent Authority shall be attached]"

Place:**Signature:**_____**Date:****Name:**_____**Stamp:**_____**OR****Model certificate for tenders for work involving possibility of sub-contracting**

"I have read the clause regarding restrictions on procurement from a bidder of a country which shares a land border with India and on sub-contracting to contracting from such countries I certify that this bidder is not from such a country or, if from such a country, has been registered with the Competent Authority and will not sub contract any work to a contract form such countries unless such contractor is registered with the Competent Authority. I hereby certify that this bidder fulfills all requirements in this regard and is eligible to be considered. [Provide Compliance for the above in Vendor Specified Commercial Terms.]"

Place:**Signature:**_____**Date:****Name:**_____**Stamp:**_____

1. PRICE SCHEDULE FOR GOODS BEING OFFERED WITHIN INDIA

Name of the Bidder _____

NIT Reference No. _____

File Reference No. _____

Sl. No.	Full Description of items with (HSN Code/SAC Code)	Quantity	Unit Price (in INR)	Total Amount (in INR)
1.				
2.				
3.				
Ex-Works Price-				
Packing & forwarding				
Transportation				
FOR (IIT ISM) Dhanbad				
<u>GST/IGST (their rate(s) as the case may be, clearly specified)</u>				
Insurance up to Destination/handover (in case of fabrication)				
Installation & Commissioning charge				
Training charges, if any				
Additional Warranty Charges, if any				
Annual Maintenance Charge, if any				

*(On the basis of the technical specifications submitted)

Total Bid Price _____

in words _____

Note :

(a) The Price schedule of optional items shall be indicated in a separate sheet in the same Performa.

(b) Cost spare parts may be indicated separately

Signature of Bidder

Name _____

Business

Address _____